



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2 Application Serial No. .... 09/517,127  
Filing Date ..... March 12, 2000  
3 Inventor ..... Scott E. Moore et al.  
Assignee ..... Micron Technology, Inc.  
4 Group Art Unit ..... Unknown  
Examiner ..... Unknown  
5 Attorney's Docket No. .... MI22-1246  
Title: "Semiconductor Processor Systems, Systems Configured to Provide a  
6 Semiconductor Workpiece Process Fluid, Semiconductor Workpiece Processing  
Methods of Preparing Semiconductor Workpiece Process Fluid, and Methods  
7 of Delivering Semiconductor Workpiece Process Fluid to a Semiconductor  
Processor"  
8  
9  
10

**INFORMATION DISCLOSURE STATEMENT**

11 References -- See Attached Form PTO-1449

12 The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No  
13 admission is made regarding whether all the submitted references are prior art.

14  
15 Respectfully submitted,

16  
17 Dated: 6/7/00

By:

  
James D. Shaurette  
Reg. No. 39,833



03C0

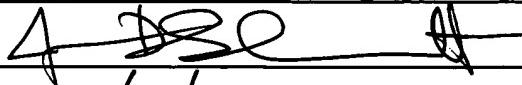
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<b>TRANSMITTAL FORM</b> <small>(to be used for all correspondence after initial filing)</small>		<b>Application Number</b> 09/517,127 <b>Filing Date</b> March 12, 2000 <b>First Named Inventor</b> Scott E. Moore et al. <b>Group Art Unit</b> Unknown <b>Examiner Name</b> Unknown
<b>Total Number of Pages in This Submission</b>	7	<b>Attorney Docket Number</b> MI22-1246

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<b>SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT</b>	
Firm or Individual name	James D. Shaurette, Reg. No. 39,833 Wells, St. John, Roberts, Gregory, & Matkin, P.S.
Signature	
Date	6/7/00

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: <b>6-7-00</b>			
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